Xilinx PACE - C:\Users\Roli\Documents\eagle\cpld_board\vhdl\door_ctrl\door1.ucf

File Edit View IOBs Tools Window Help

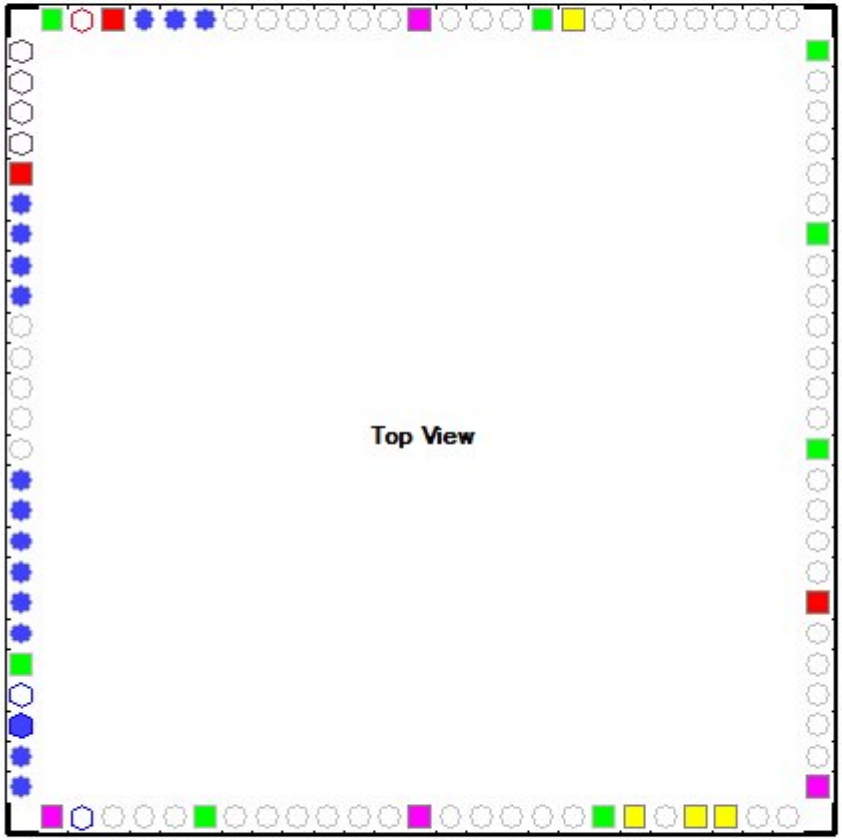
Design Browser

- I/O Pins
  - clk
  - in1a
  - in1b
  - in2a
  - in2b
  - in3a
  - in3b
  - in4a

Design Object List - I/O Pins

I/O Name	I/O Direction	Loc	Function Block	Macro
clk	Input	P23	3	2
in1a	Input	P15	1	8
in1b	Input	P16	1	9
in2a	Input	P17	1	11
in2b	Input	P18	1	12
in3a	Input	P19	1	14
in3b	Input	P20	1	15
in4a	Input	P24	3	5
in4b	Input	P25	3	6
led1	Output	P6	2	11
led2	Output	P7	2	12
led3	Output	P8	2	14
led4	Output	P9	2	15
out1	Output	P95	4	14
out2	Output	P96	4	15
out3	Output	P97	4	17

Package Pins for xc95144xl-TQ100-5



Package View